

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE :: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 07/12/2021

LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. V-SEMESTER SUPPLE(VR14 REGULATION)
OCTOBER-2021

14EI2505-5-Instrumentation Engineering(CE)

- NIL -

14EC3502 – Pulse and Switching Circuits

- NIL -

14EC3504 – Digital Communications


- NIL -


14EC3507 - Transmission Lines and Waveguides

- NIL -

14EI3503 – Microcontrollers and Applications

- NIL -


CONTROLLER OF EXAMINATIONS
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OF REVALUATION OF B.TECH. VI -SEMESTER SUPPLE VR14 REGULATION)
EXAMINATIONS HELD IN OCTOBER -2021

14CS3604 – Theory of Computation

- NIL -

14EC3601 – Linear Integrated Circuits and Applications

- NIL -

14EC3603 – Antennas and Wave Propagation


- NIL -


14EC3604 – VLSI Design

- NIL -

14EC3605 – Digital Signal Processing

- NIL -


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EXAMINATIONS HELD IN OCTOBER -2021


14CS4705D – Software Project Management
- NIL -

14EE3702 – Switch Gear & Protection
- NIL -

14EI4705/4 – Digital Image Processing
178W5A1010

14ME3703 – Refrigeration and Air Conditioning
- NIL -


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EXAMINATIONS HELD IN OCTOBER -2021

14EI4801B- Biomedical Instrumentation

- NIL -


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