

VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS), VIJAYAWADA  
 REVISED TIME TABLE FOR B.TECH., IV SEMESTER REGULAR & SUPPLEMENTARY EXAMS (VR17 REGULATION), SEPTEMBER, 2021

VR17

DATE: 06/09/2021

TIME: 2.00 PM to 5.00 PM

DATE	CE	CSE	ECE	EEE	EIE	IT	ME
13/09/2021 Monday	17CE3401 Building Materials and Construction		17MA1401 Probability Theory & Random Processes		17EI3401 Analytical Instrumentation	17IT3401 Statistics with R	
14/09/2021 Tuesday		17CS3401 Discrete Mathematical Structures		17EE3401 Linear Control Systems			17ME3401 Fluid Mechanics and Hydraulic Machines
15/09/2021 Wednesday	17CE3402 Concrete Technology		17EC3402 Analog ICs and Applications		17EI3402 ICs and Applications	17IT3402 DBMS	
16/09/2021 Thursday		17CS3402 Web Technologies		17EE3402 Electrical Measurements			17ME3402 Machine Dynamics
17/09/2021 Friday	17CE3403 Mechanics of Materials		17EC3403 Electromagnetic Theory and Transmission Lines (for 2017-18 batch)		17EI3403 Industrial Instrumentation	17IT3403 Design & Analysis of Algorithms	
			17EC3403A Transmission Lines & Waveguides (for 2018-19 & 2019-20 Batches)				
18/09/2021 Saturday		17CS3403 Advanced Data Structures		17EE3403 Electrical Machines- II			17ME3403 Engineering Metallurgy

20/09/2021 Monday	17CE3404 Hydraulics and Hydraulic Machines		17EC3404 Analog Communications		17EI3404 Electrical and Electronic Measurements	17IT3404 Python Programming	
21/09/2021 Tuesday		17CS3404 Computer Organization		17EE3404 Digital Signal Processing			17ME3404 Electrical and Electronics Engg.
22/09/2021 Wednesday			17EC3406 Pulse and Switching Circuits		17EI3406 Digital Circuits and Systems	17IT3406 Operating Systems	
23/09/2021 Thursday		17CS3406 Operating Systems					
25/09/2021 Saturday		17CS3408 Python Programming					

(Dr. A V RATNA PRASAD)

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V.R.Siddhartha Engineering College

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