

Registration form

Name: _____

Designation: _____

Institution & Department: _____

Address: _____

Qualifications: _____

Age: _____ Sex: Male/Female

Telephone No. _____

Email: _____

Signature of the Candidate _____
Signature of the Head of the Institution _____

Note:

- ❖ Registration is free (A refundable deposit of Rs 1000/- is collected from each participant and refunded after successful completion of the FDP)
- ❖ Demand Draft/CBS-Cheque drawn in favor of "Electronics and ICT Academy, MNIT Jaipur" payable at Jaipur is to be submitted.
- ❖ Certificate for participation as well as for satisfactory performance will be given to the participants subject to fulfillment of attending all sessions, submission of assignments and clearing the test(s).
- ❖ Filled in Registration Forms are mandatory for participation
- ❖ Attendance is mandatory for all the sessions to get participation certificate.

Chief Patron

Sri N. Venkateswarlu, President,
Siddhartha Academy of General & Technical
Education (SAGTE)

Patrons

Sri P. Lakshmana Rao, Secretary,
(SAGTE)
Sri M. Rajaiah, Vice-President, SAGTE and
Convener,
V R Siddhartha Engineering College

College Advisory Committee

Dr A V Ratna Prasad, Principal
Dr N N Sastry, Professor, Dean, R & D
Dr B Panduranga Rao, Dean, Student
Affairs

Convener

Dr. D. Rajeswara Rao
Professor & Head, Department of CSE
Mob: 8142032551
Email: hodcse@vrsiddhartha.ac.in

Chief Coordinator:

Dr K Suvarna Vani
Professor
Department of CSE, VRSEC
Mobile: 9989892694
suvarnavanik@vrsiddhartha.ac.in

Venue @ CSE Department Seminar Hall



One Week FDP
On
Natural Language Processing
6th – 10th January, 2020
Organized By



**Department of Computer
Science & Engineering**

**Velagapudi Ramakrishna
Siddhartha Engineering College
(Autonomous)**

Sponsored by Siddhartha Academy of General and
Technical Education (SAGTE)
Vijayawada, AP -520007

www.vrsiddhartha.ac.in

In Collaboration with

E & ICT Academies @



Please register @ <http://online.mnit.ac.in/eict/clogin.php>

About VRSEC:

Velagapudi Ramakrishna Siddhartha Engineering College (VRSEC) was established in the year 1977 as the first self-financing engineering college in the state of A.P. It is located in a vast expanse of 24 acres of land on the outskirts of Vijayawada city at a distance of about 6 Kms from the city centre. The college offers 7 undergraduate programmes and 10 Post graduate programmes. The faculty at present are 350, comprising of well qualified professionals with varied experience and background. The college boasts 64 MoUs with industrial partners and has established Skill Development Centre in association with Govt of AP. The college was accredited by NAAC with A grade and obtained NBA accreditation for all UG and 5 PG programmes in OBE format approved by Washington Accord, USA.

About CSE Department:

Established in the year 1986, the department of CSE offers B.Tech programme in Computer Science and Engineering and one M.Tech Programme in Computer Science and Engineering. The B.Tech program was accredited by NBA of AICTE in 2005, 2008 and 2015. The M.Tech program was accredited by NBA in 2015. The department has a team of highly qualified senior faculty with specializations in core areas of Computer Science and Engineering. The CSE department has been sanctioned projects from various organizations like UGC, AICTE, DST, Dept of Space-NRSC and ADRIN. It has a faculty strength of 46 and an intake of 180 students for UG and 36 for PG programmes.

About E & ICT Academy:

With the objectives of 'Digital India' and 'Make in India', Department of Electronics and Information Technology, Government of India has instituted four Electronics and ICT Academies with one academy at IIITDM Jabalpur in partnership with IIITM Gwalior MANIT Bhopal, and IIT Indore. The Academy aims at running Training Programmes for faculty, students and unemployed graduates in fundamental areas as well as on the cutting edge technologies. In addition, the Academy will conduct specialized/customized Training Programmes and Research Promotion workshops for corporate sector and educational institutions.

About NLP:

Natural language processing (NLP) is a branch of Artificial Intelligence that helps computers to understand, interpret and manipulate human language. NLP draws from many disciplines, including Computer Science and computational linguistics, in its pursuit to fill the gap between human communication and computer understanding.

Expected Outcomes:

After the successful completion of the FDP, the participants will be able to:

1. Apply different steps of Natural Language Processing namely Lexical Analysis, Parsing, Semantic Analysis, Syntactic Processing.
2. Ability to perform sequence to sequence tasks
3. Ability to understand Dialog Systems, Unification- Based NLP and semantics.

Target Audience:

The FDP is open to all Faculty, System Analyst/Computer Programmer and Technical Staff from Govt./Govt. Aided/Self Financing Engineering Colleges, Polytechnic Colleges and Degree Colleges. The total number of participants are limited to 20, and registration is on a first come, first served basis.

Program Objectives:

- To provide exposure to the recent trends in Artificial intelligence and Natural language processing.
- To enable the participant to define a research problem and develop a suitable methodology for addressing the problem.
- To make the participants understand, learn and use various analysis methods (tools) in NLP research.

Contents Covered:

- ❖ Introduction and Text Classification
- ❖ Language Modeling & Sequence Tagging
- ❖ Vector Space Models of Semantics
- ❖ Syntactic Processing
- ❖ Sequence to Sequence Tasks
- ❖ Dialog Systems
- ❖ Unification-based NLP Semantics

Please register @ <http://online.mnit.ac.in/eict/clogin.php>