

VELAGAPUDI RAMAKRISHNA  
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7  
(AUTONOMOUS)

Date: 23/05/2024

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF  
REVALUATION OF B.TECH. III SEMESTER SUUPLE (VR20 REGULATION)  
EXAMINATIONS HELD IN APRIL- 2024**

**20BS3101C – Transformation & Numerical Methods**


- NIL -

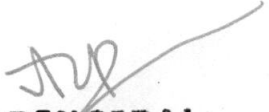
**20EC3302 – Analog Electronics**

- NIL -

**20EE3302 – Electronic Circuits**

- NIL -

  
**CONTROLLER OF EXAMINATIONS**  
Velagapudi Rama Krishna  
SIDDHARTHA ENGINEERING COLLEGE  
(AUTONOMOUS)  
VIJAYAWADA-520 007.A.P

  
**PRINCIPAL**  
**V.R.Siddhartha Engineering College**  
AUTONOMOUS  
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EXAMINATIONS HELD IN APRIL -2024**

**20CS52505A –Web Programming**


- NIL -


**20HS5103 – Engineering Economics And Management**

- NIL -

**20ME5302 – Design of Machine Elements**

- NIL -

  
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