

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 20/10/2022

LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. II SEMESTER REGULAR & I,II SEMESTER SUPPLE
(VR20 REGULATION) EXAMINATIONS HELD IN AUGUST -2022

20BS2101 - Laplace Transforms & Integral Calculus

218W1A0522

20BS2101A - Probability & Statistics For Data Science

- NIL -

20BS1102/20BS2102 -Engineering Chemistry

218W1A5423 218W1A0522

20ES2103A - Object Oriented Programming Using Python

- NIL -

20ES2103B - Python Programming

- NIL -

20ES2103C - Programming Essentials in Python

218W1A5450

20ES2104A - Basics of Electronics Engineering

218W1A5433 218W1A1228 218W1A12C4

20ES2104B - Circuit Analysis

218W1A0471 218W1A04L4

20ES2104C - Network Theory

218W1A1048

20ES2104D - Engineering Mechanics

- NIL -

20ES2104E - Network Analysis

- NIL -

20ES2104F - Engineering Mechanics-II

- NIL -

20HS2105 - Technical English & Communication Skills

- NIL -

20ES2105 - Engineering Graphics

218W1A5435 218W1A5449 218W1A0476 218W1A04B9 218W1A04H0 218W1A04H9
218W1A04I5 218W1A04I8 218W1A04J4 218W1A04K0 218W1A1268

20BS1101 - Matrices & Differential Calculus

218W1A04F8

20ES1103- Programming for Problem Solving

- NIL -

20ES1104 - Basics of Electrical Engineering

- NIL -

CONTROLLER OF EXAMINATIONS
Velagapudi Rama Krishna
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA-520 007.A.P


PRINCIPAL
V.R.Siddhartha Engineering College
AUTONOMOUS
VIJAYAWADA-520 007