

VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS)
VIJAYAWADA- 520 007
1/4 B.Tech 1 & 2 SEMESTER (SUPPLEMENTARY) PRACTICAL EXAMINATION TIME TABLE
APRIL - 2018

DATE	TIME	PLACE	14CS1152/14CS1252 Basic computing Lab / C Programming Lab
21.04.2018	09.00 AM to 12.00 PM	S&H-BLOCK- I (FMC-I)	SUPPLE

SUPPLE

B.Tech ¼ Ist Sem Supple

Total: 1

14CS1152:

168W1A0161

B.Tech ¼ IInd Sem Supple

Total: 17

14CS1252:


168W1A0563, 148W1A02B5, 168W1A0217, 168W1A0218, 168W1A0242, 168W1A0277,
168W1A1051, 168W1A1067, 168W1A1080, 168W1A1082, 168W1A1085, 148W1A0390,
168W1A0325, 168W1A0353, 168W1A0367, 168W1A03D4, 168W1A03H7


B.Tech ¼ IInd Sem Supple:

Total : 1

VR10 FY2052:

118W1A10B4


PRINCIPAL
V.R.Siddhartha Engineering College
AUTONOMOUS
VIJAYAWADA-520 007

Yours sincerely,

(Dr.A.Ratnakar)
In-Charge, I/IV B.Tech
INCHARGE
First Year B.Tech
V.R.Siddhartha Engineering College
VIJAYAWADA - 520 007